

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

1. (currently amended) A system ~~(300, 302)~~ for performing a switch-over in data communication in accordance with a protection switching data communication principles, ~~characterized in that~~

wherein the system comprises a configurable integrated circuit of a unit ~~(300)~~ of the data communication for signaling a need ~~(400)~~ for the switch-over in real time based data communication to a configurable integrated circuit of a protecting pair unit ~~(302)~~ of said unit of the data communication, and

wherein said configurable integrated circuit of said protecting pair unit is structured and arranged to perform the switch-over independently of a CPU, when the switch-over is needed.

2. (currently amended) A system according to claim 1, wherein the system provides the signaling between the units without a participation of the CPU.

3. (currently amended) A system according to claim 1, wherein the configurable integrated circuit comprises at least

one of application-specific integrated circuit ~~(ASIC)~~ and field-programmable gate array ~~(FPGA)~~.

4. (original) A system according to claim 1, wherein the protection switching comprises a protected LSP based on a working connection and a protecting connection.

5. (original) A system according to claim 1, wherein said unit comprises a working unit in accordance with a LSP working connection and the protection pair unit comprises a protection unit in accordance with a LSP protection connection.

6. (original) A system according to claim 1, wherein the signal comprises a protection message for delivering that the data communication of a receiving unit is at least one of faulty and unfaulty.

7. (original) A system according to claim 1, wherein the real time based data communication presumes the switch-over to take place in less than 50 milliseconds from an occurrence of a connection fault.

8. (original) A system according to claim 1, wherein the data communication comprises at least one of Internet Protocol, Ethernet, and MPLS for real time telecommunication services.

9. (original) A system according to claim 1, wherein Multiprotocol Label Switching is contained as a bearer for the data communication.

10. (original) A system according to claim 9, wherein Multiprotocol Label Switching operates as a backbone for IP based data communication.

11. (original) A system according to claim 1, wherein the real time based data communication is such that human senses any application based on the real time based data communication substantially immediate.

12. (original) A system according to claim 1, wherein the data communication takes place between a source computing entity and a sink computing entity.

13. (currently amended) A network entity ~~(300, 302)~~ for performing a switch-over in data communication in accordance with a protection switching data communication principles, ~~characterized in that~~

wherein the network entity comprises a configurable integrated circuit of a unit ~~(300)~~ of the data communication for signaling a need ~~(400)~~ for the switch-over in real time based data communication to a configurable integrated circuit of a protecting pair unit ~~(302)~~ of said unit of the data communication, and

wherein said configurable integrated circuit of said protecting pair unit is structured and arranged to perform the switch-over independently of a CPU, when the switch-over is needed.

14. (currently amended) ~~[[An]]~~ A configurable integrated circuit card for performing a switch-over in data communication in accordance with a protection switching data communication principles,

wherein the configurable integrated circuit card of the data communication ~~is adapted to signal~~ signals a need ~~(400)~~ for the switch-over in real time based data communication to a configurable integrated circuit of a protecting pair card of said card of the data communication, and

wherein said configurable integrated circuit of said protecting pair unit is structured and arranged to perform the switch-over independently of a CPU, when the switch-over is needed.

15. (currently amended) A method for performing a switch-over in data communication in accordance with a protection switching data communication principles, ~~characterized in that,~~ the method ~~comprises the step of~~ comprising:

signaling a need ~~(400)~~ for the switch-over in real time based data communication from a configurable integrated circuit of a unit ~~(300)~~ of the data communication to a configurable integrated circuit of a protecting pair unit ~~(302)~~ of said unit of the data communication, and

performing the switch-over by said configurable integrated circuit of said protecting pair unit independently of a CPU.

16. (original) A method according to claim 15, further comprising before the step of signaling the step of detecting a connection fault in the data communication at the unit.

17. (currently amended) A method according to ~~claims 15-16~~ claim 15, further comprising the step of receiving the need at the protecting pair unit and performing the switch over by activating the data communication on the protecting pair unit.

18. (currently amended) A computer readable medium encoded with a computer program product comprising a program of instructions executable by a computing system for processing a switch-over in data communication in accordance with a protection switching data communication principles, the computer program product comprising:

computer program code for causing the system to signal a need ~~(400)~~ for the switch-over in real time based data communication from a configurable integrated circuit of a unit ~~(300)~~ of the data communication ~~[[for]]~~ to ~~an~~ a configurable integrated circuit of a protecting pair unit ~~(302)~~ of said unit of the data communication, and

computer program code for causing the system to perform the switch-over by said configurable integrated circuit of said protecting pair unit independently of a CPU when the switch-over is needed.

19. (new) The system according to claim 1, wherein said unit comprises a card and said protecting pair unit comprises another card.

20. (new) The system according to claim 5, wherein said working unit comprises a card and said protecting unit comprises another card.

21. (new) The system according to claim 1, wherein said unit is structured and arranged to send a protection message to said protecting pair unit, said protecting pair unit is structured and arranged to interpret the message and perform the switch-over, if necessary.